EtherCAT Technology Group
Members

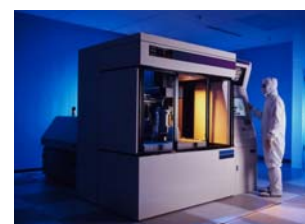**Kick-Off Meeting ETG Semiconductor Technical Working Group**
Wednesday, Oct 26 + Thursday, Oct 27, 2011, Milpitas, Silicon Valley, CA, USA

August 31, 2011

Dear ETG Member,

If you have activities in the **Semiconductor and Flat Panel Display (FPD) manufacturing industry**—including **Photovoltaics (PV) and LED manufacturing**, you will know that EtherCAT is growing rapidly in these industries. Several major equipment and tool makers have adopted EtherCAT, and many Semi component suppliers have first products readily available.

Following the request from our members, we are now establishing an **ETG Semiconductor Technical Working Group (Semi TWG)**, which will address Semi specific device profiles, installation guidelines, and device tests in order to meet the special requirements of this industry.

ETG Semiconductor TWG Kick-Off Meeting
Date: October 26/27, 2011
Location: Embassy Suites Milpitas - Silicon Valley, CA, USA

REGISTER ►
online today!

For this kick-off meeting we have secured the active participation of premium tool makers, in order to ensure that the results meet their requirements; as of today, presentations are scheduled by **Applied Materials, LAM Research**, **Aixtron** and **Brooks Automation** to talk about their expectations regarding EtherCAT enabled Semi components.
Other presentations are being considered by ETG members **Tokyo Electron** and **Dainippon Screen** as many Semi related EtherCAT users and potential users are expected to join this meeting.

So if you are active in the Semi industry, you should not miss this Kick-off Meeting: we intend to shape the future work of the TWG by defining work plans, setting priorities and road maps. The Kick-off meeting itself will be preceded by an EtherCAT technology introduction, and followed by an EtherCAT implementation seminar. In the evening of day-one there will be a Get-Together for networking and discussion.

Please sign up online as soon as possible: we may be forced to close registration early if participation exceeds meeting room capacity. We have blocked rooms in the meeting hotel— but they may go quickly since we expect many participants.

Registration deadline is Monday, Oct 17, 2011.

Please find the agenda and venue information below. If you have any questions, please do not hesitate to contact us.

Best regards,

Martin Rostan
Executive Director
EtherCAT Technology Group

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ETG **Semiconductor Technical Working Group Kick-Off Meeting**
Embassy Suites Milpitas—Silicon Valley, CA, USA

Agenda: Wednesday, Oct 26, 2011

9 a.m. - 12 p.m.	EtherCAT and ETG Introductory Seminar—not only for newcomers *Speakers: Joseph E. Stubbs, ETG Representative North America, and Martin Rostan, ETG*
1 p.m.	Registration + Official Start of Semi TWG Kick-Off Meeting
1:30 p.m.	Welcome, EtherCAT Adoption Worldwide *Martin Rostan, Executive Director, ETG*
	EtherCAT-enabled Advanced Control Architecture for Semiconductor Manufacturing Equipment *Dmitry A. Dzilno, Director of Engineering Synexis Controls Group, Applied Materials, Santa Clara, CA*
	Brooks's network selection for semiconductor manufacturing equipment and our vision and expectations for the ETG Semi TWG *Krzysztof Majczak, MSEE, Chief System Architect, Brooks Automation, Chelmsford, MA , USA*
3:00 p.m.	Coffee Break
3:30 p.m.	EtherCAT @ Aixtron: today and tomorrow *Karl-Heinz Büchel, Program Manager Corporate Control Systems, Aixtron, Herzogenrath, Germany*
	From System design to Deployment: Flexible Configuration *Brian Lajevardi, Senior Technical Program Manager, Lam Research, Fremont, CA, USA*
	Dainippon Screens EtherCAT requirements* *nn. Dainippon Screen, Kyoto, Japan* *to be confirmed
	Ideas regarding EtherCAT Mass Flow Controllers , RF Generators, and other Devices *Brett Hoogensen et al, Applied Materials, Santa Clara, CA*
	Existing Standards to Streamline the ETG Semi TWG *Daniel R. Judd, President and CTO, Arlington Laboratory, Burlington, MA, USA*
	Open Discussion
6 p.m. - open end	Get Together: Networking Event

Agenda: Thursday, Oct 27, 2011

9 a.m.	Formation of the Semiconductor Technical Working Group ⇒ Workplan, Priorities, Timeline ⇒ Chairperson ⇒ Next Steps
1 p.m.	Official End of Semi TWG Kick-Off Meeting
2 p.m. - 5 p.m.	EtherCAT Implementation Overview Seminar ⇒ How to implement an EtherCAT device ⇒ What to study, who helps, how does ETG support ⇒ Resources and effort estimation ⇒ Successful conformance testing

ETG Semi TWG Kick Off **Meeting Venue**
Wednesday, October 26 + Thursday, October 27, 2011

Location

Embassy Suites Milpitas - Silicon Valley
901 East Calaveras Boulevard, Milpitas, CA, USA
Tel: +1-408-942-0400, Fax: 1-408-262-8604
www.milpitassiliconvalley.embassysuites.com

The Embassy Suites Milpitas is located between the cities of San Jose and Fremont, conveniently adjacent to three of the Silicon Valley's major highways and a short drive from San Jose International Airport (SJC) and downtown San Jose. The hotel boasts nine stories of elegance with 266 spacious two-room suites, free cooked-to order breakfast, a complementary manager's reception, indoor pool and fitness center. The hotel provides complimentary parking facilities and also a complimentary airport shuttle service to the nearby SJC airport.

Since the ETG membership is free of charge, the venue costs are shared by the participants. We will therefore ask for a cost contribution of $50, payable at the meeting. This will cover meeting room costs + beverages.

For meeting attendants we have blocked a number of rooms for a special rate. Please book your room as soon as possible, because there is only a limited quota.

For your room booking, please either use this button or use the Booking Code "BEC" when you call reservations.

Hotel Room Booking ▶

For meeting registration, please go here:
www.ethercat.org/registration_semi_twg

Meeting Registration ▶

How to get there
- From I-880 - Take the Exit 237-Calaveras and travel east approximately two miles to Hillview Dr. Turn left onto Hillview and right onto Hillview Court into hotel parking lot.
- From I-680 - Take Exit 237-Calaveras and travel west one block. The hotel is on the right.
- From Hwy 101 Southbound and Hwy 101 Northbound (after passing downtown San Jose) - Take Exit 237-Alviso to Milpitas. Travel east on 237 for 9-10 miles to Hillview Dr. Turn left onto Hillview and right onto Hillview Court into hotel parking lot.
- From Hwy 101 northbound (before reaching San Jose) - Take Hwy 680 North to the Hwy 237-Calaveras Blvd. exit. West on Calaveras